



08/355652

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Docket: 0756-1139

#41B  
2-22-95  
RGS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Divisional Application of )  
Shunpei YAMAZAKI et al. )  
Based on Serial No. 08/142,048 ) Art Unit: 1104  
Filed: December 14, 1994 ) Examiner: C. Chaudhari  
For: LASER PROCESSING METHOD, )  
METHOD FOR FORMING A )  
FLASH MEMORY, INSULATED )  
GATE SEMICONDUCTOR )  
DEVICE AND METHOD FOR )  
FORMING THE SAME ) Date: December 14, 1994

PRELIMINARY AMENDMENT

Honorable Commissioner of Patents and Trademarks  
Washington, D.C. 20231

Sir:

Please preliminarily amend the subject application as follows:

IN THE CLAIMS:

Cancel claim 1 and add new claims 21-52.

--21. A laser processing method comprising:  
forming a gate member on a semiconductor;

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